

Notice of References Cited	Application/Control No. 10/505,287		Applicant(s)/Patent Under Reexamination TOREN, WILLEM JAN	
	Examiner Eric B. Chen		Art Unit 1765	Page 1 of 1

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*	B	US-2002/0137331	09-2002	Chang, Ching-Yu	438/637
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 639 and 646.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.